

Fig. 1
 Fig. 1A Fig. 1B

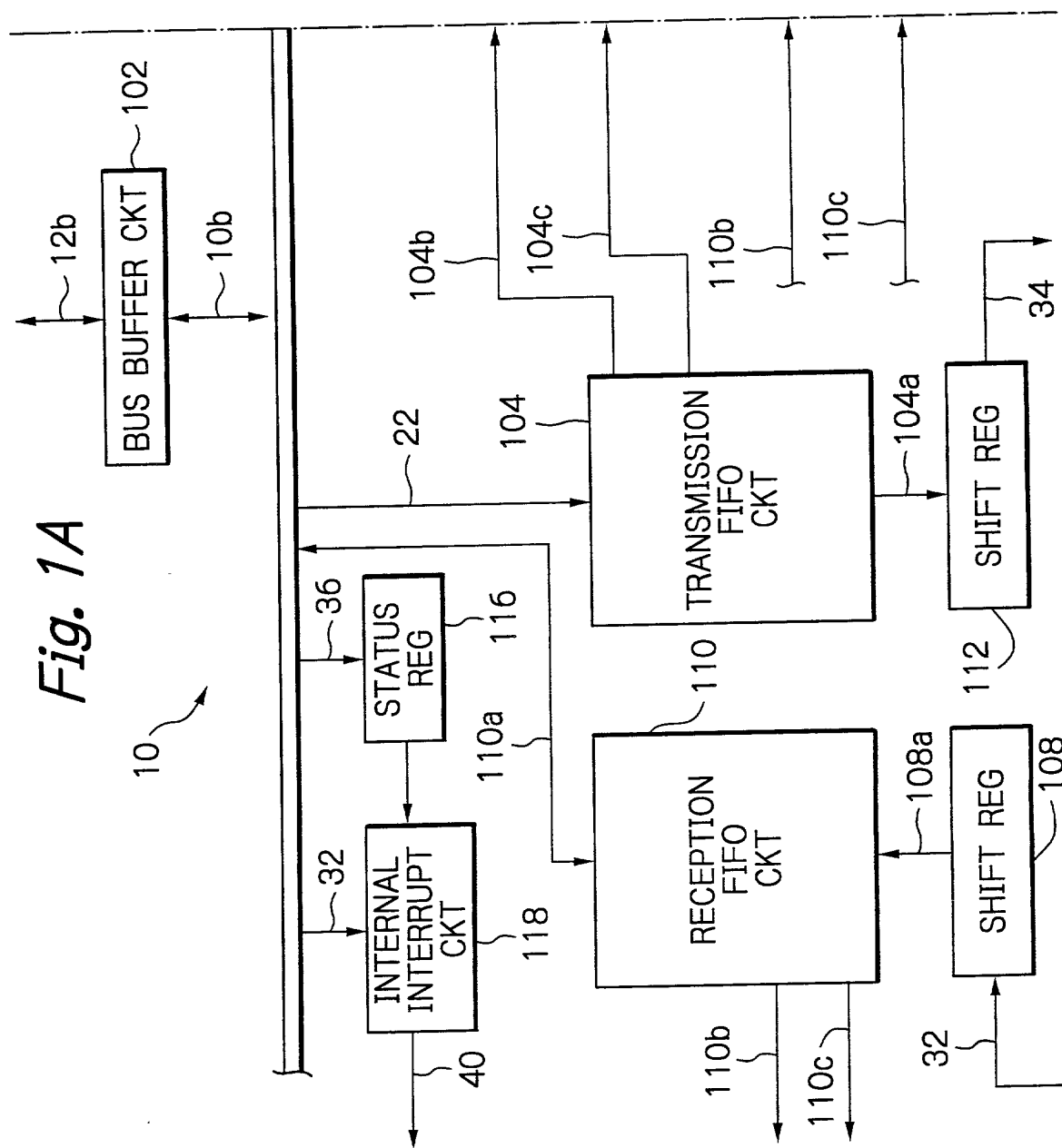


Fig. 1B is a block diagram of a system 10. The system 10 is divided into two main sections: a transmission path (left) and a reception path (right). The transmission path includes a WP Controller (14) and an RP Controller (18), both connected to a common bus 10a. The RP Controller (18) is also connected to a Tx TRG (14a). The reception path includes a WP Controller (16) and an RP Controller (20), both connected to the common bus 10a. The RP Controller (20) is also connected to an Rx TRG (14b). The Tx TRG (14a) and Rx TRG (14b) are connected to a common bus 114. Signal lines 104b, 104c, 106b, 110b, 106a, 106c, 114a, and 114b are shown.

Fig. 2

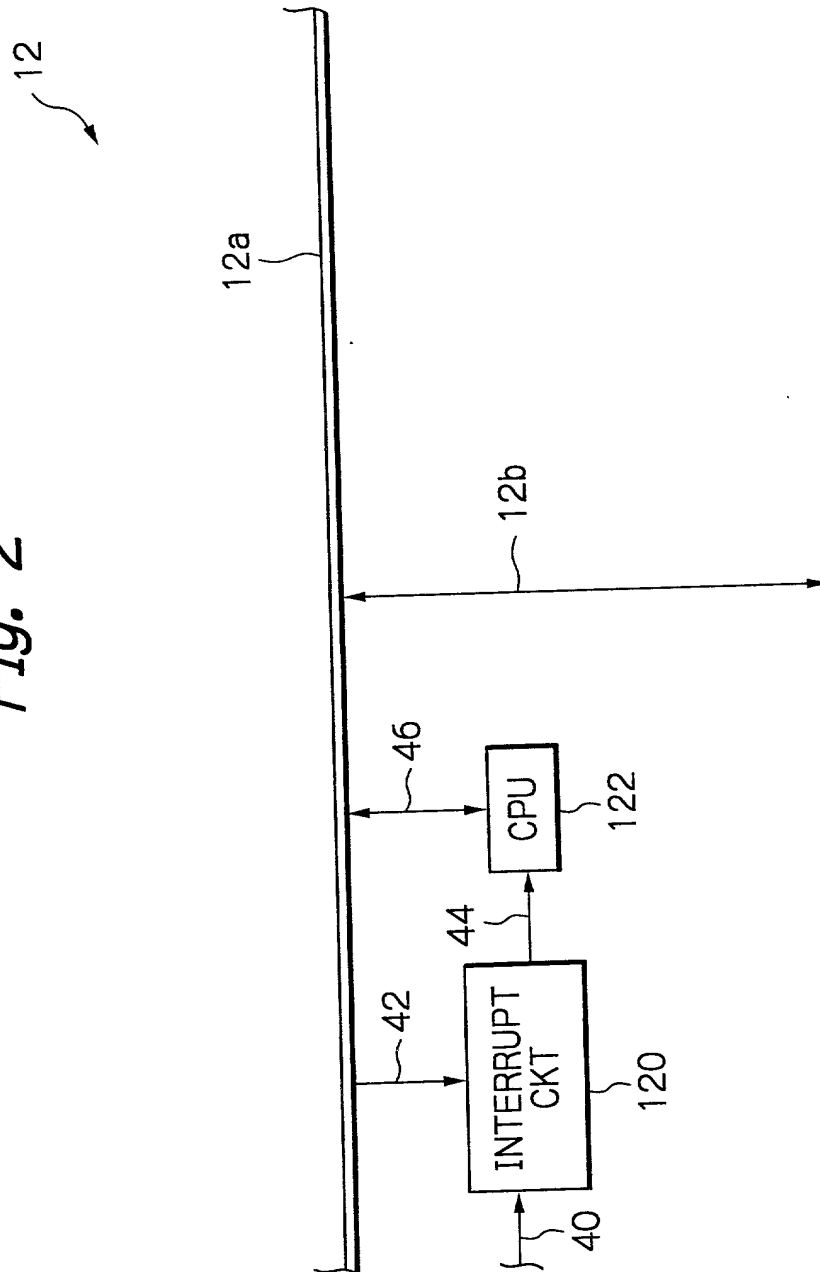


Fig. 3

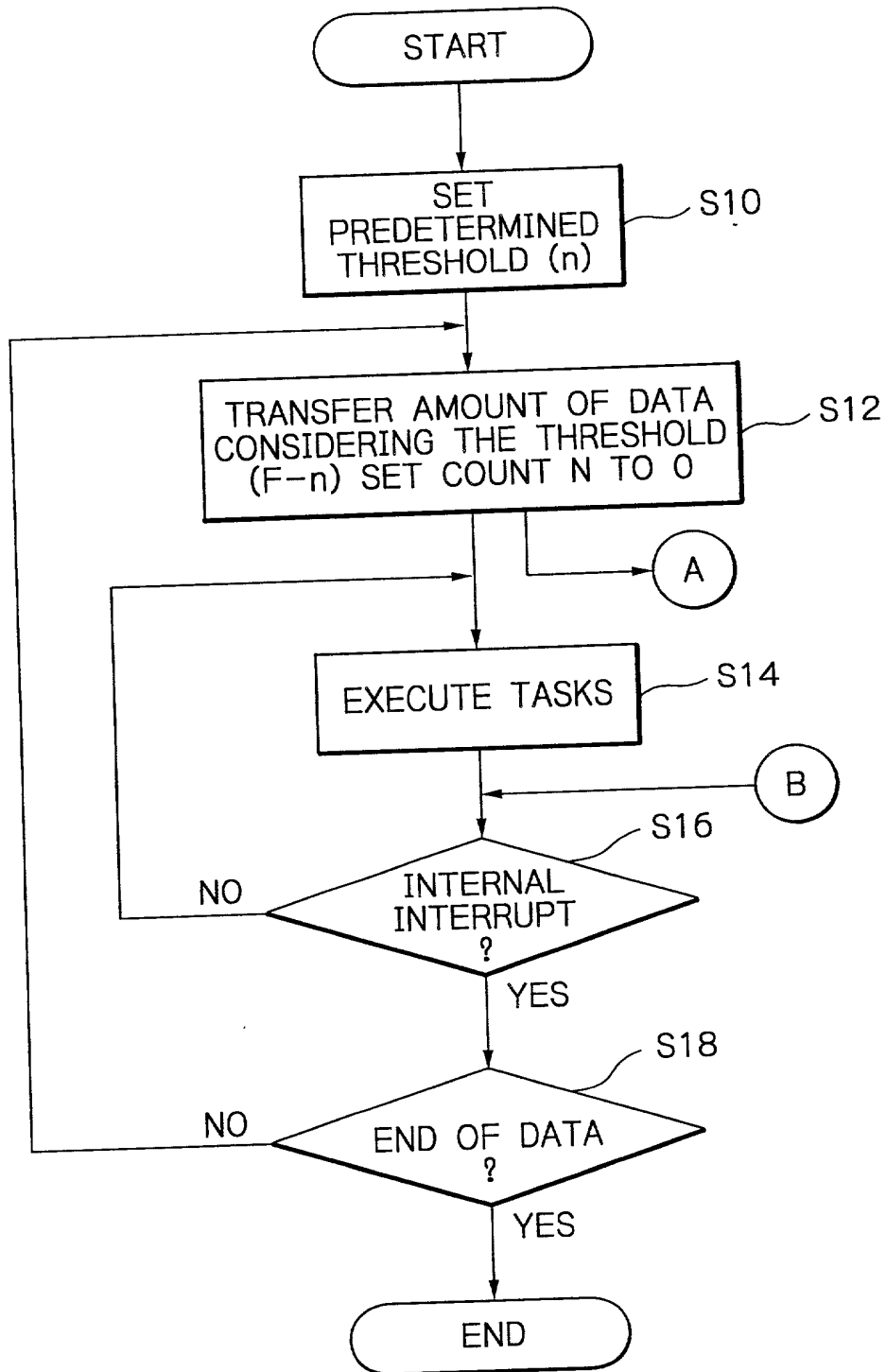


Fig. 4

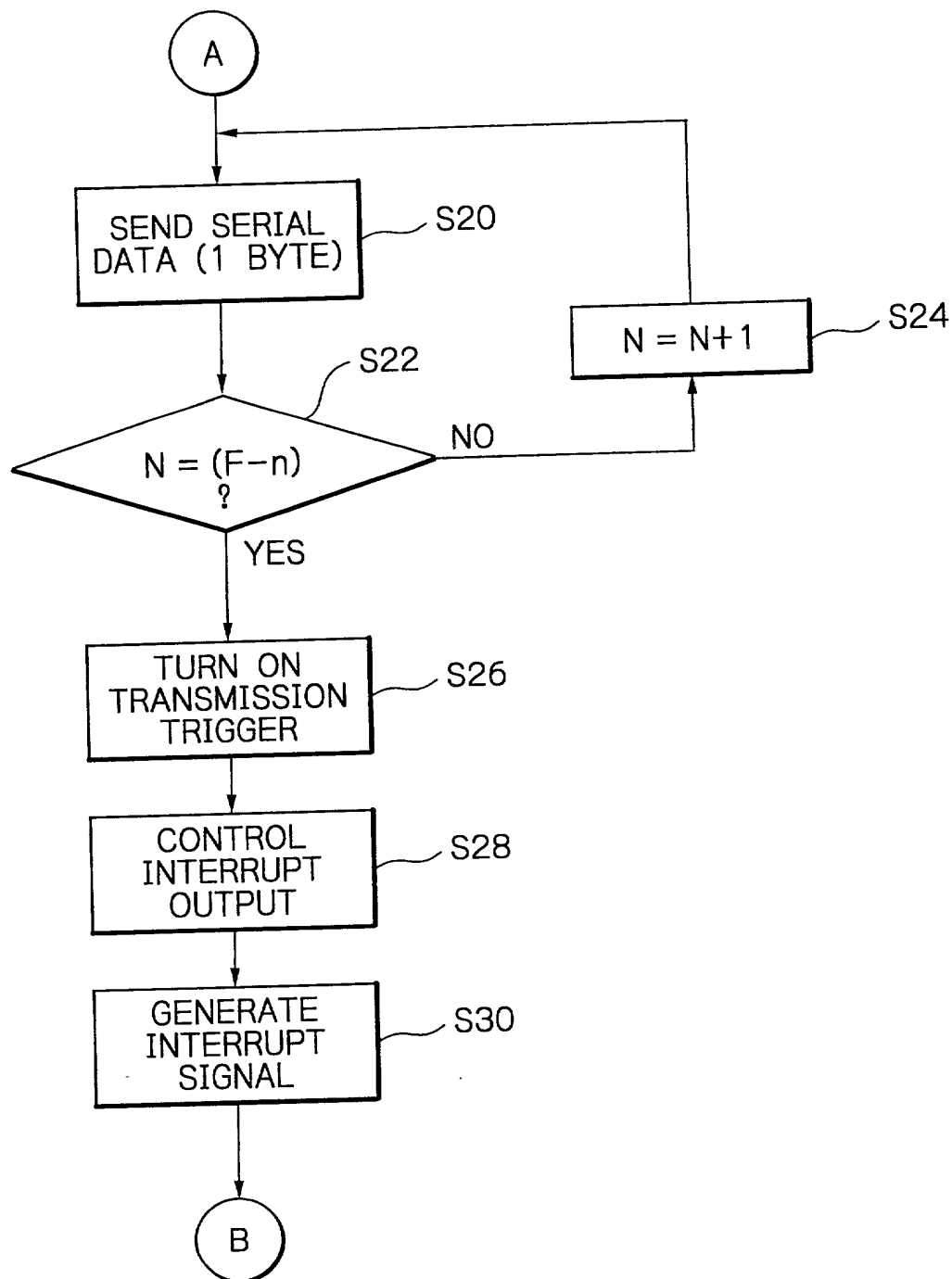


Fig. 5

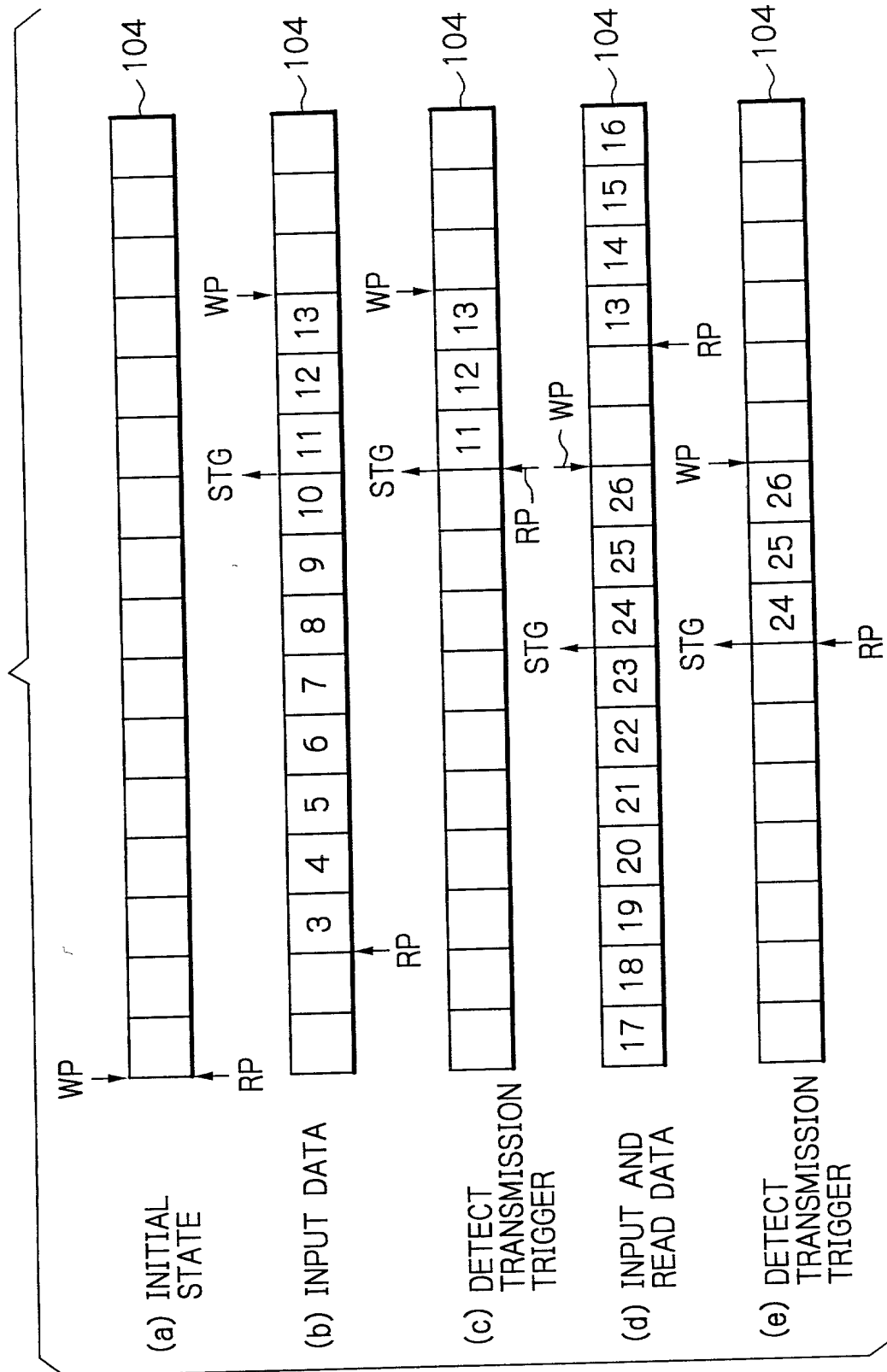


Fig. 6

